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| SERIAL NUMBER 10/767,001 | FILING DATE 01/29/2004 RULE | CLASS 710 | GROUP ART UNIT 2112 | ATTORNEY DOCKET NO. IDTL012US0-1856 |
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APPLICANTS

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** CONTINUING DATA ***** *NONE*
CEL

** FOREIGN APPLICATIONS ***** *NONE*
CEL

IF REQUIRED, FOREIGN FILING LICENSE GRANTED
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|---|---------------------------|------------------------|-----------------------|----------------------------|
| Foreign Priority claimed <input type="checkbox"/> yes <input checked="" type="checkbox"/> no | STATE OR COUNTRY CA | SHEETS DRAWING 8 | TOTAL CLAIMS 24 | INDEPENDENT CLAIMS 3 |
| 35 USC 119 (a-d) conditions met <input type="checkbox"/> yes <input checked="" type="checkbox"/> no <input type="checkbox"/> Met after allowance | | | | |
| Verified and Acknowledged Examiner's Signature _____ Initials <i>CEL</i> | | | | |

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TITLE
 Buffer bypass circuit for reducing latency in information transfers to a bus

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|-----------------------------------|---|---|
| FILING FEE RECEIVED 842 | FEES: Authority has been given in Paper No. _____ to charge/credit DEPOSIT ACCOUNT No. _____ for following: | <input type="checkbox"/> All Fees <input type="checkbox"/> 1.16 Fees (Filing) <input type="checkbox"/> 1.17 Fees (Processing Ext. of time) <input type="checkbox"/> 1.18 Fees (Issue) <input type="checkbox"/> Other _____ <input type="checkbox"/> Credit |
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